

FIG 1

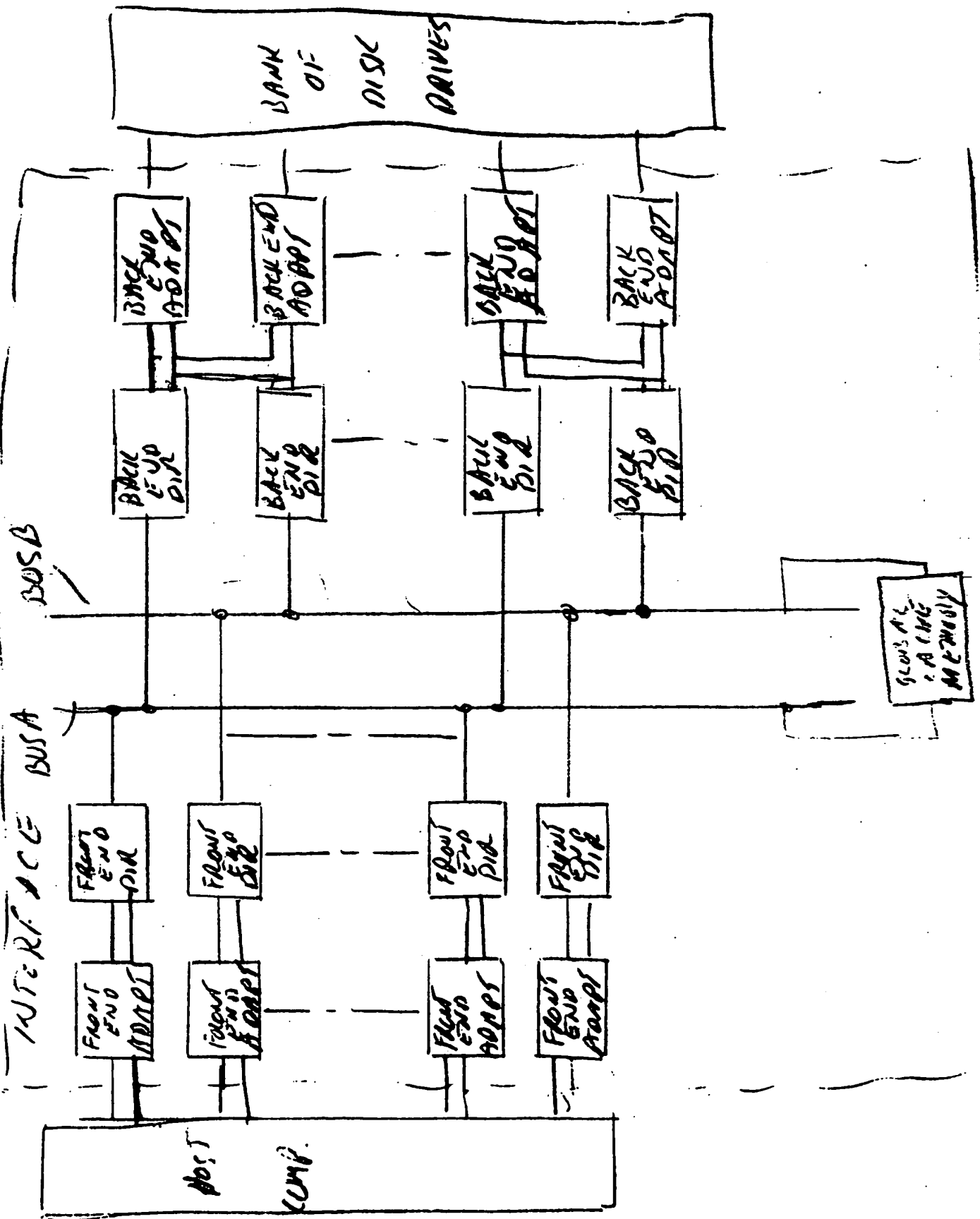
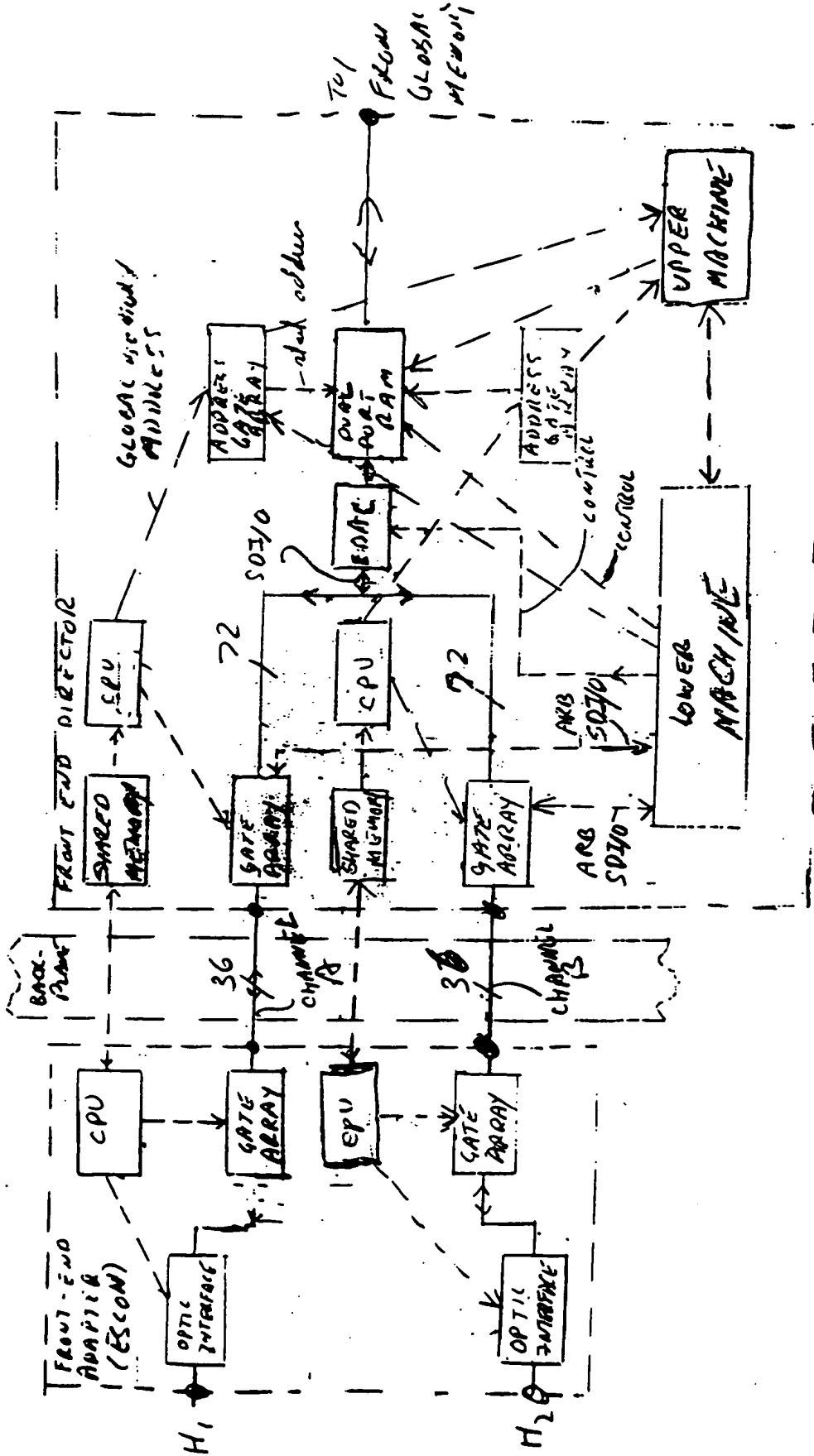


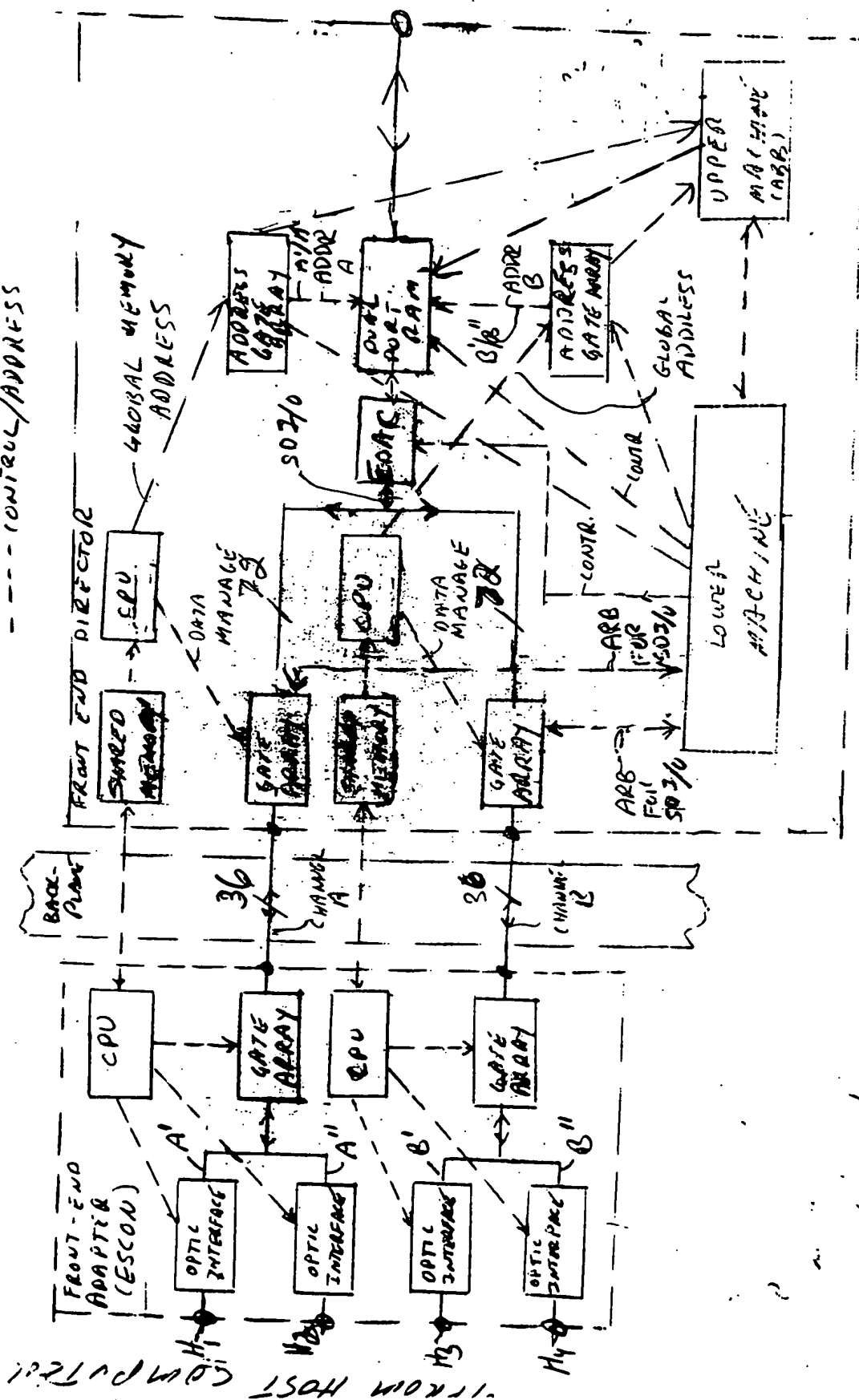
FIG. 2

DATA
CONTROL/ADDRESS



UPPER MACHINE
LOWER MACHINE

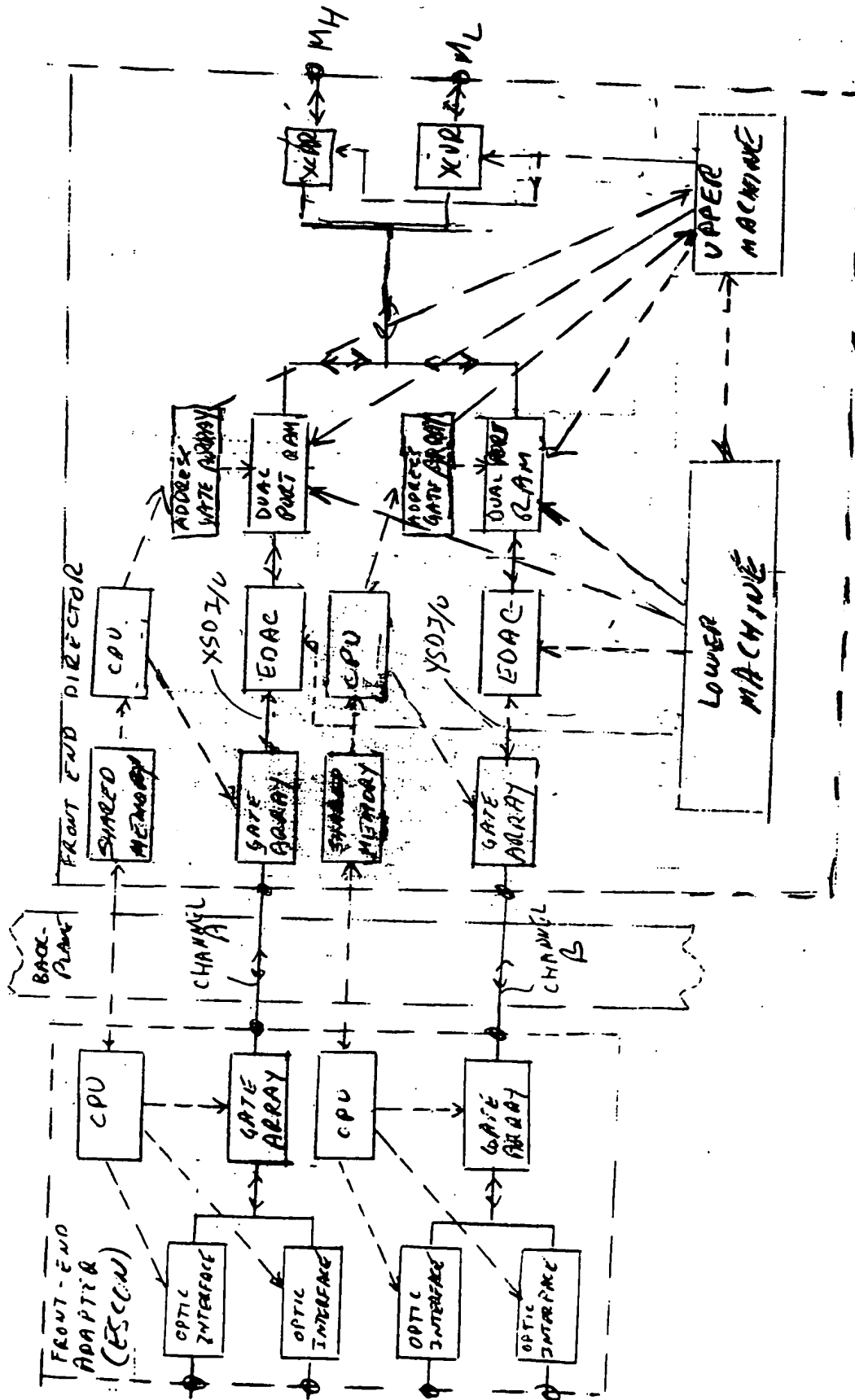
FIG. 3



Prior Art

Sum

FIG. 4



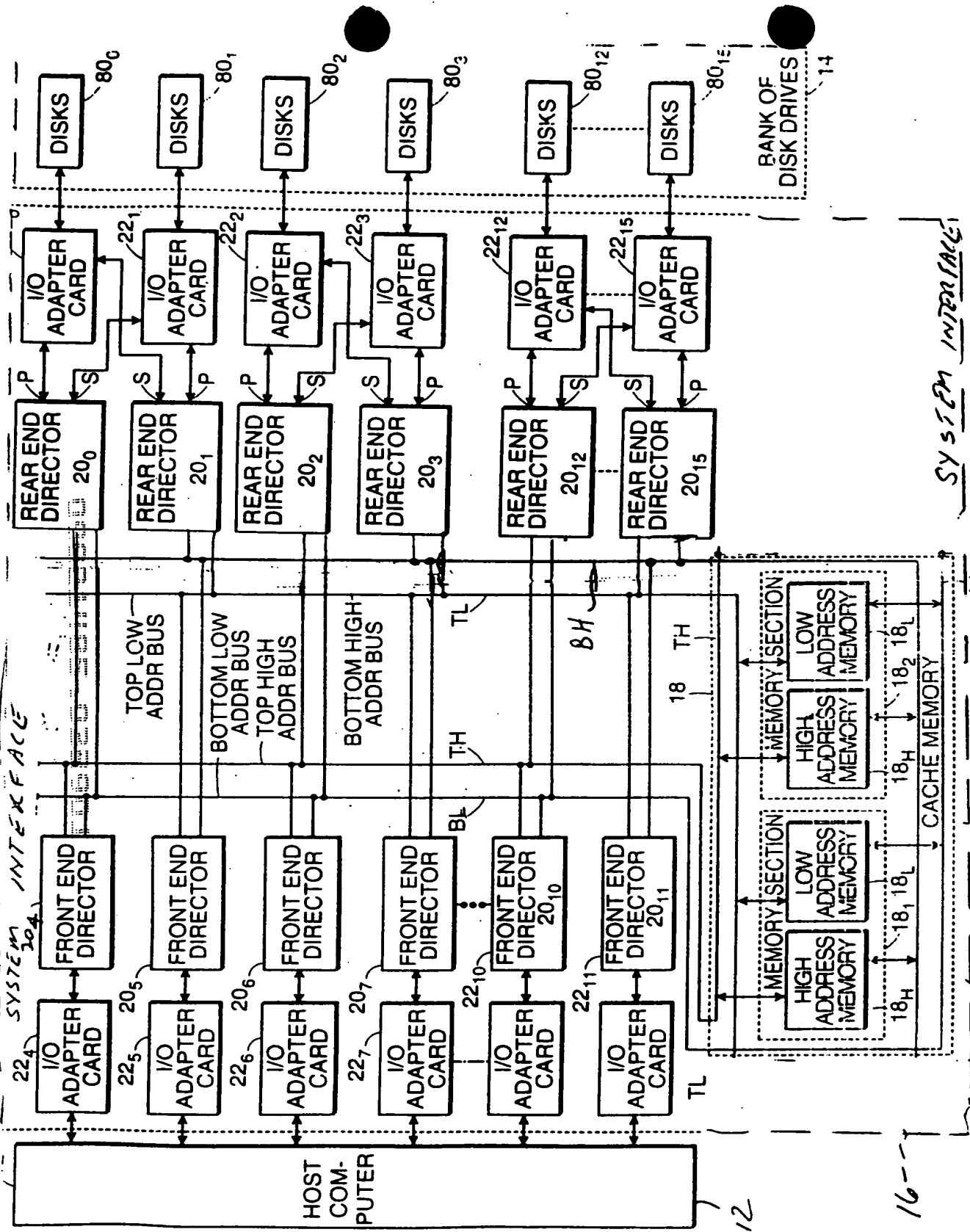
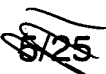


Fig 5



6
G
E

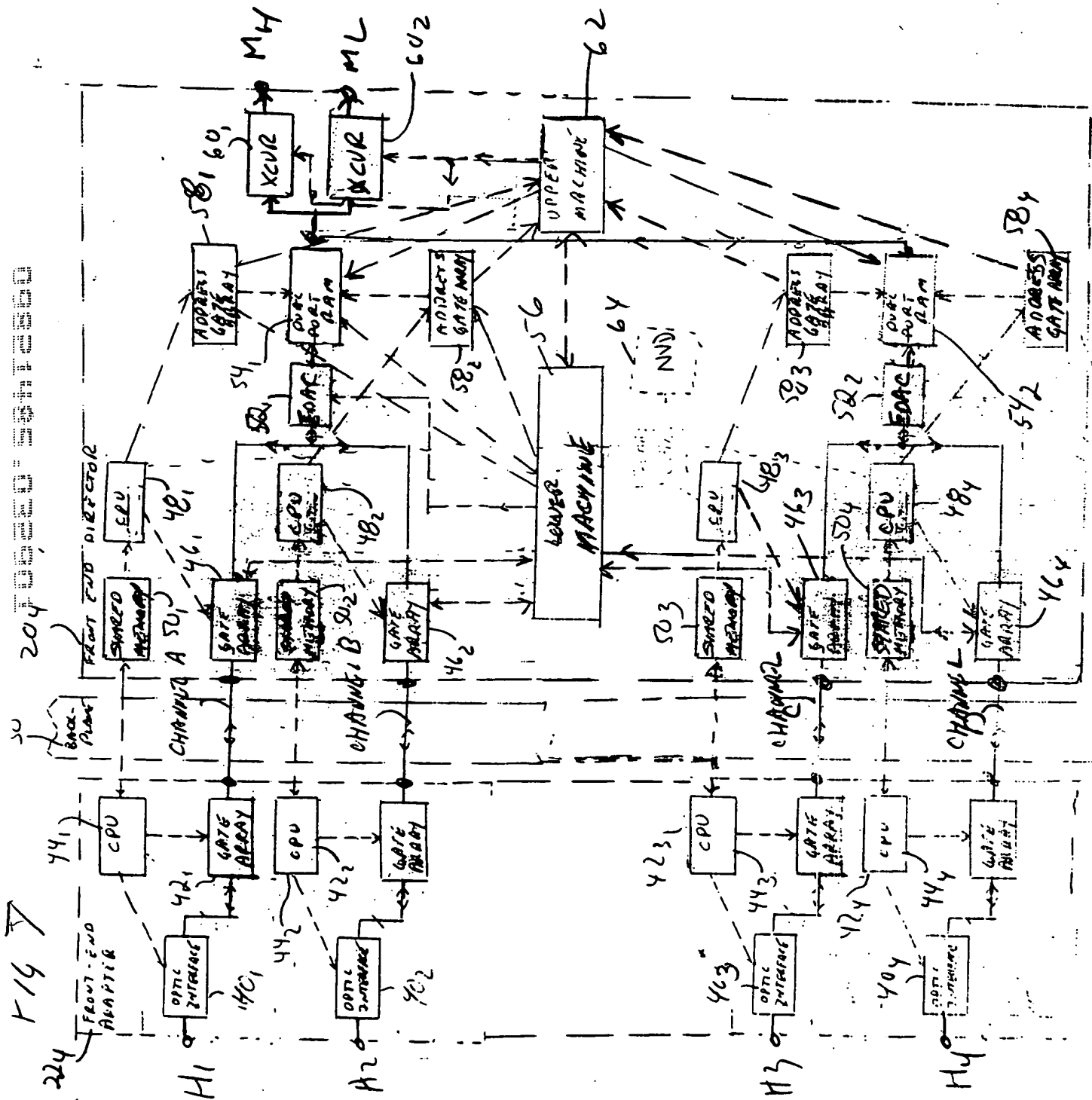
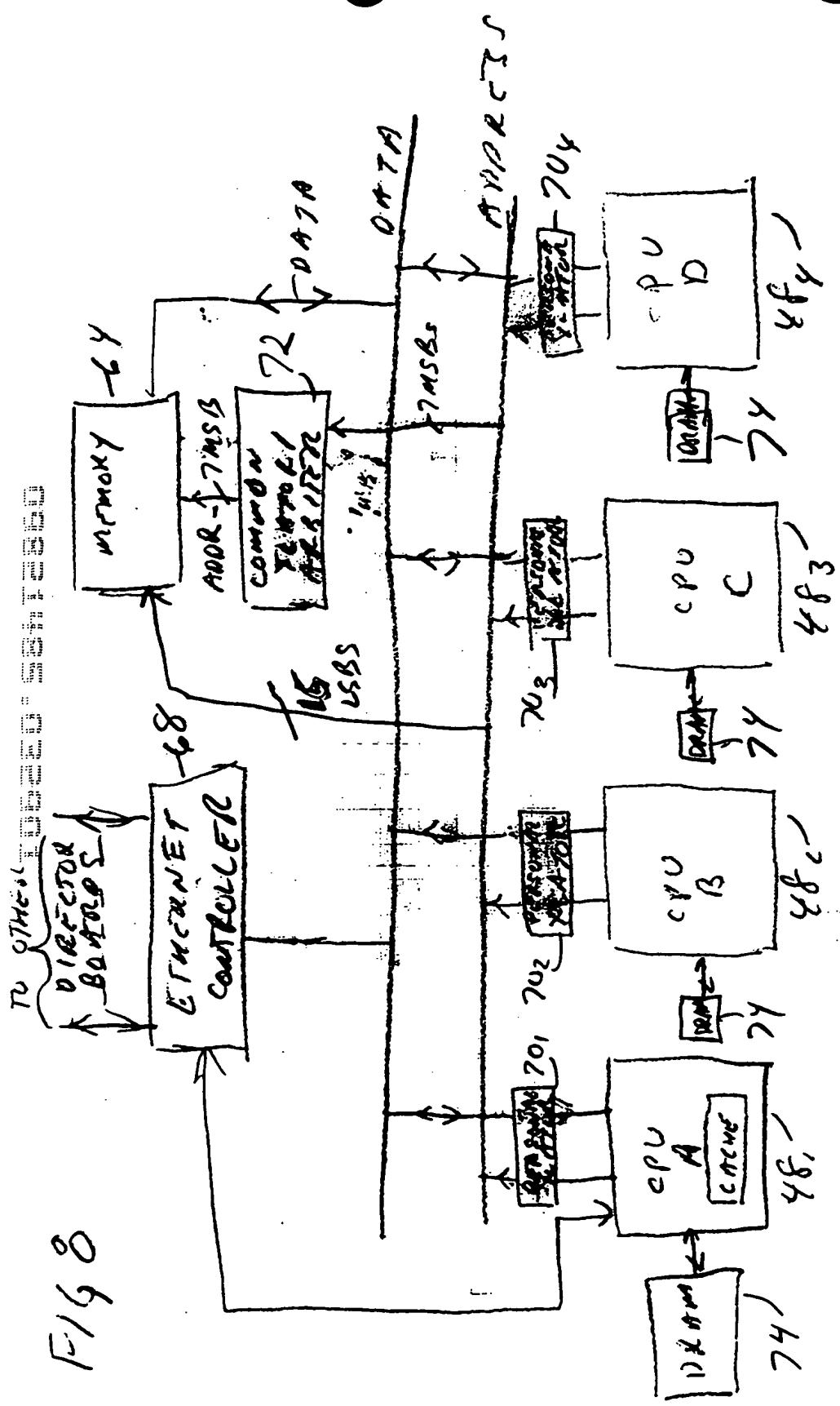


FIG 8



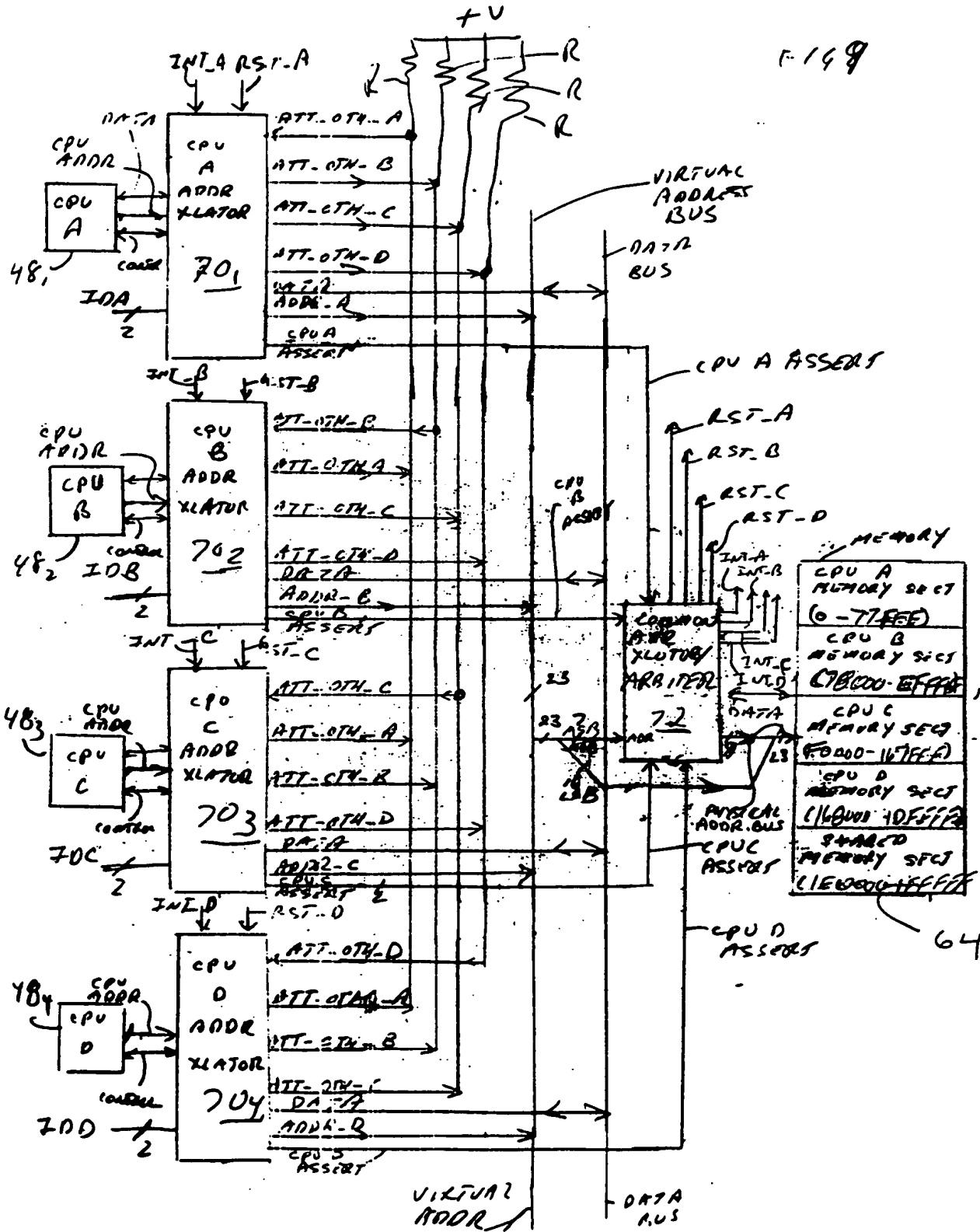
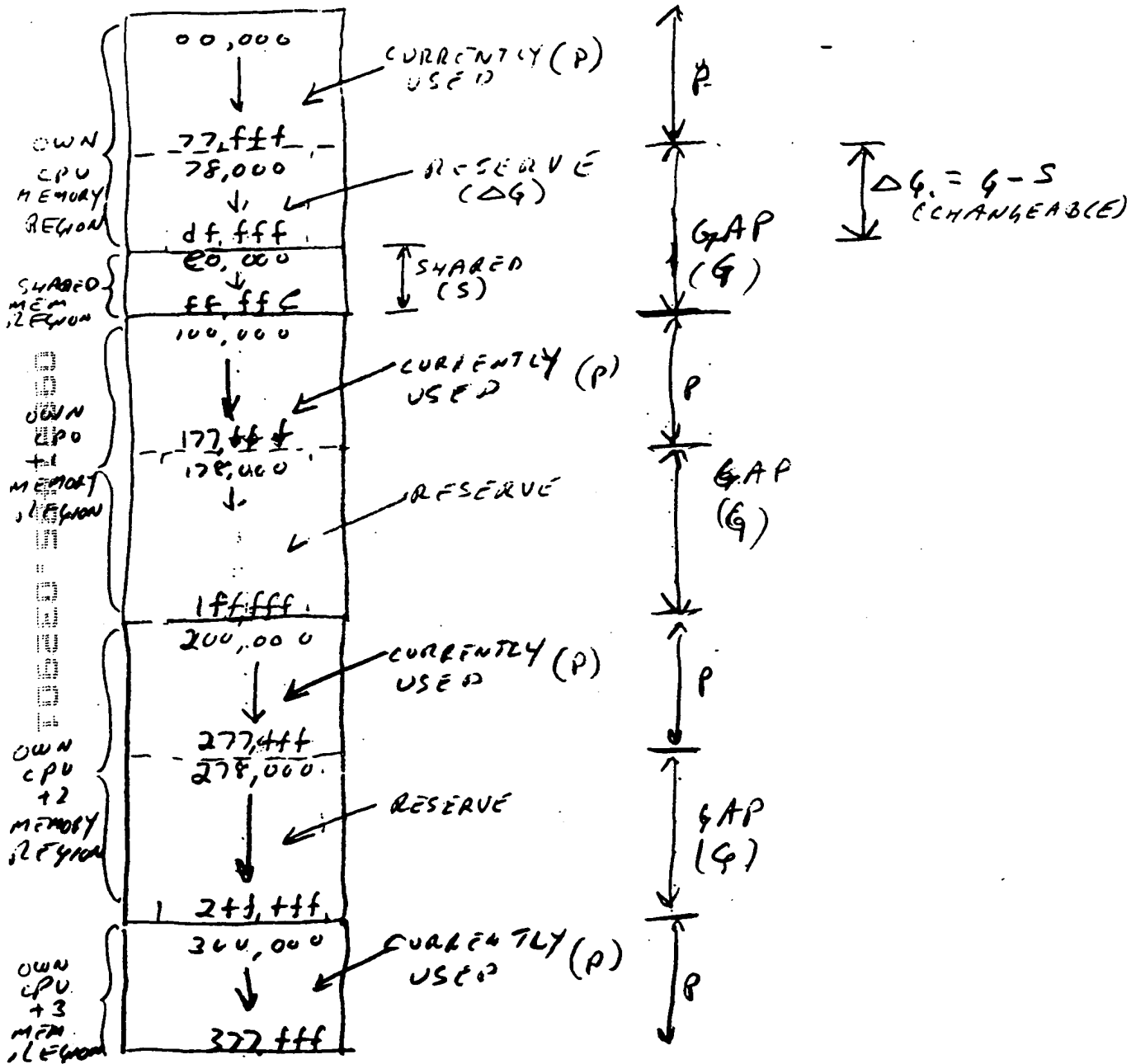
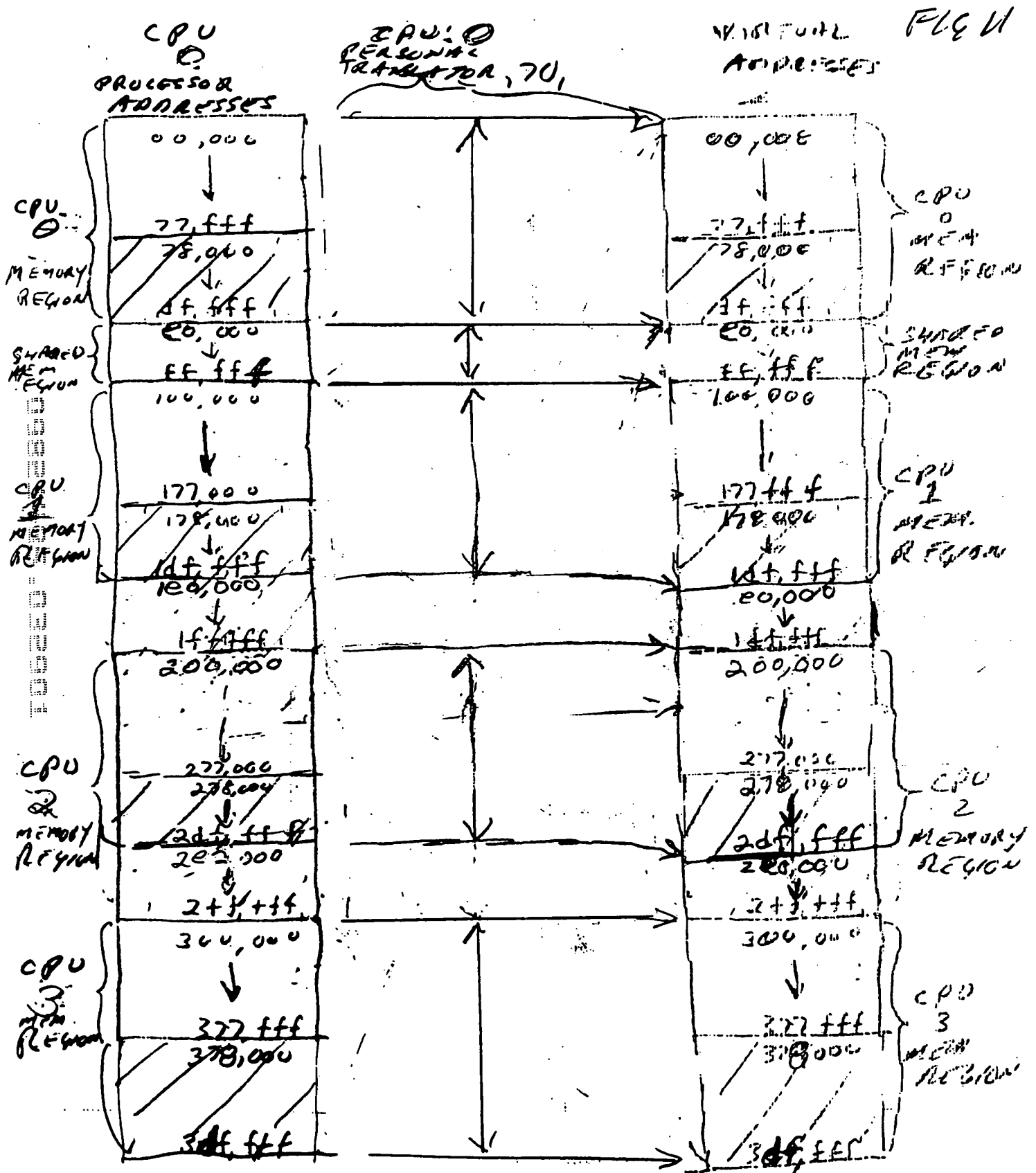
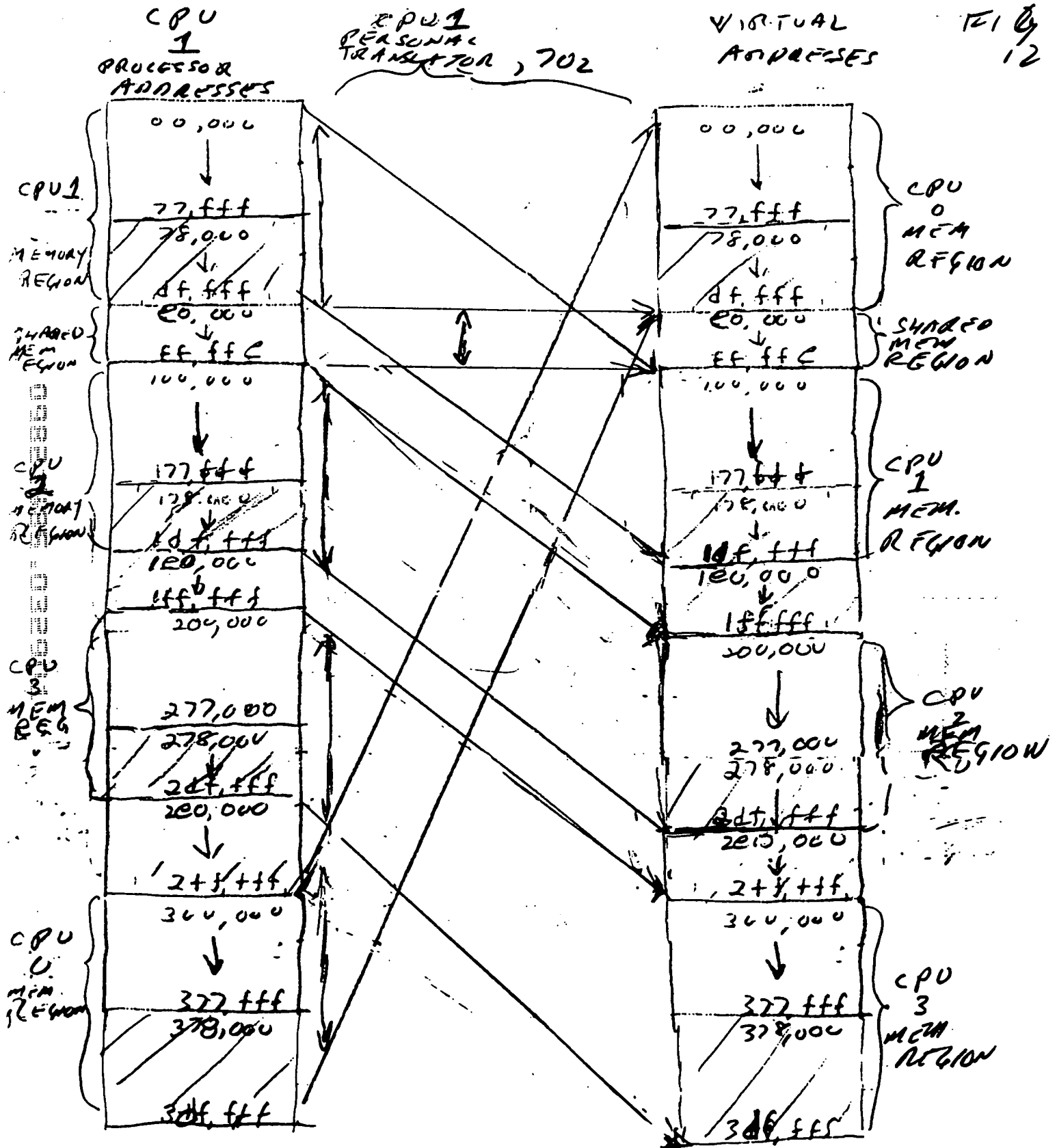


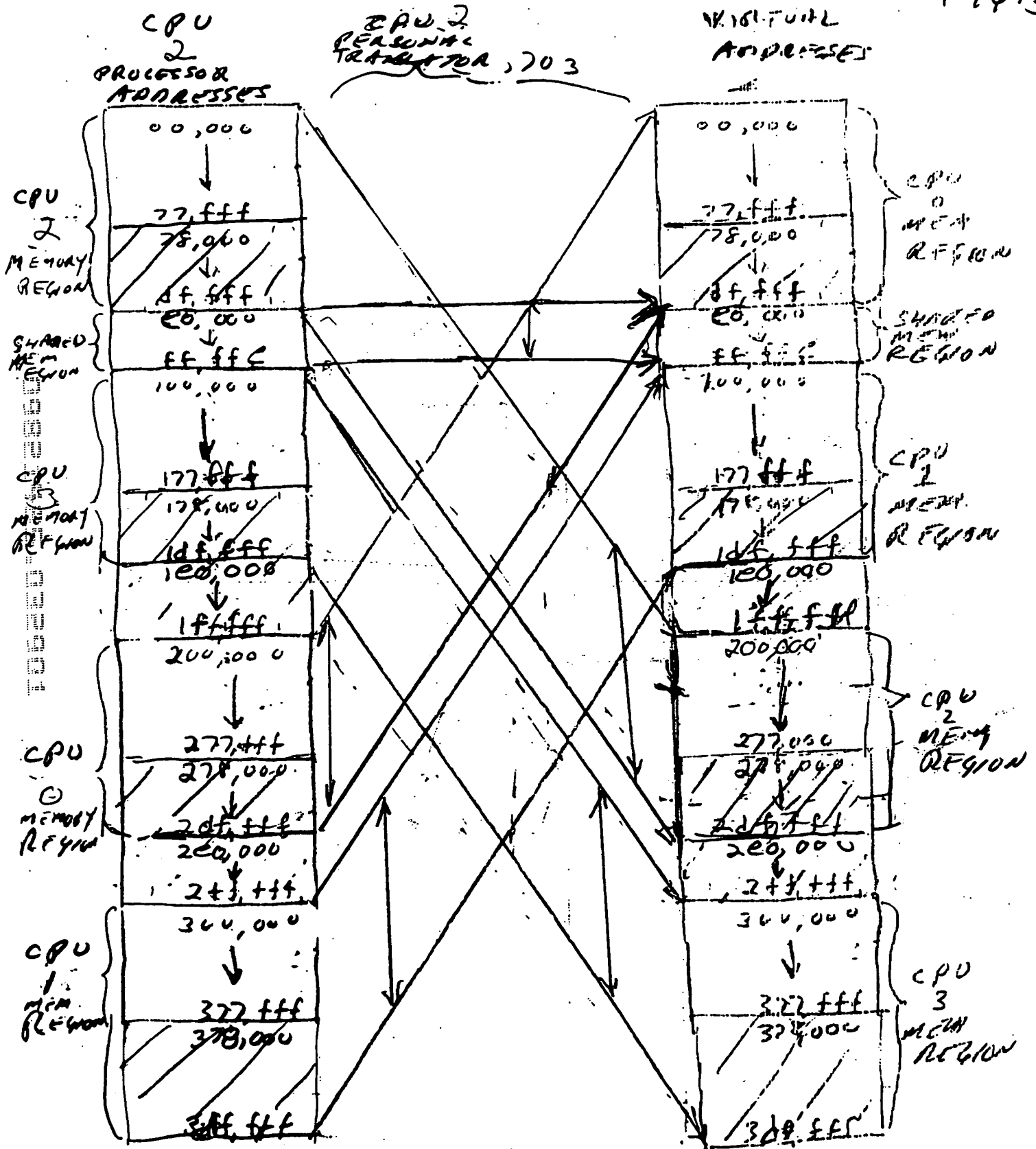
FIG 10

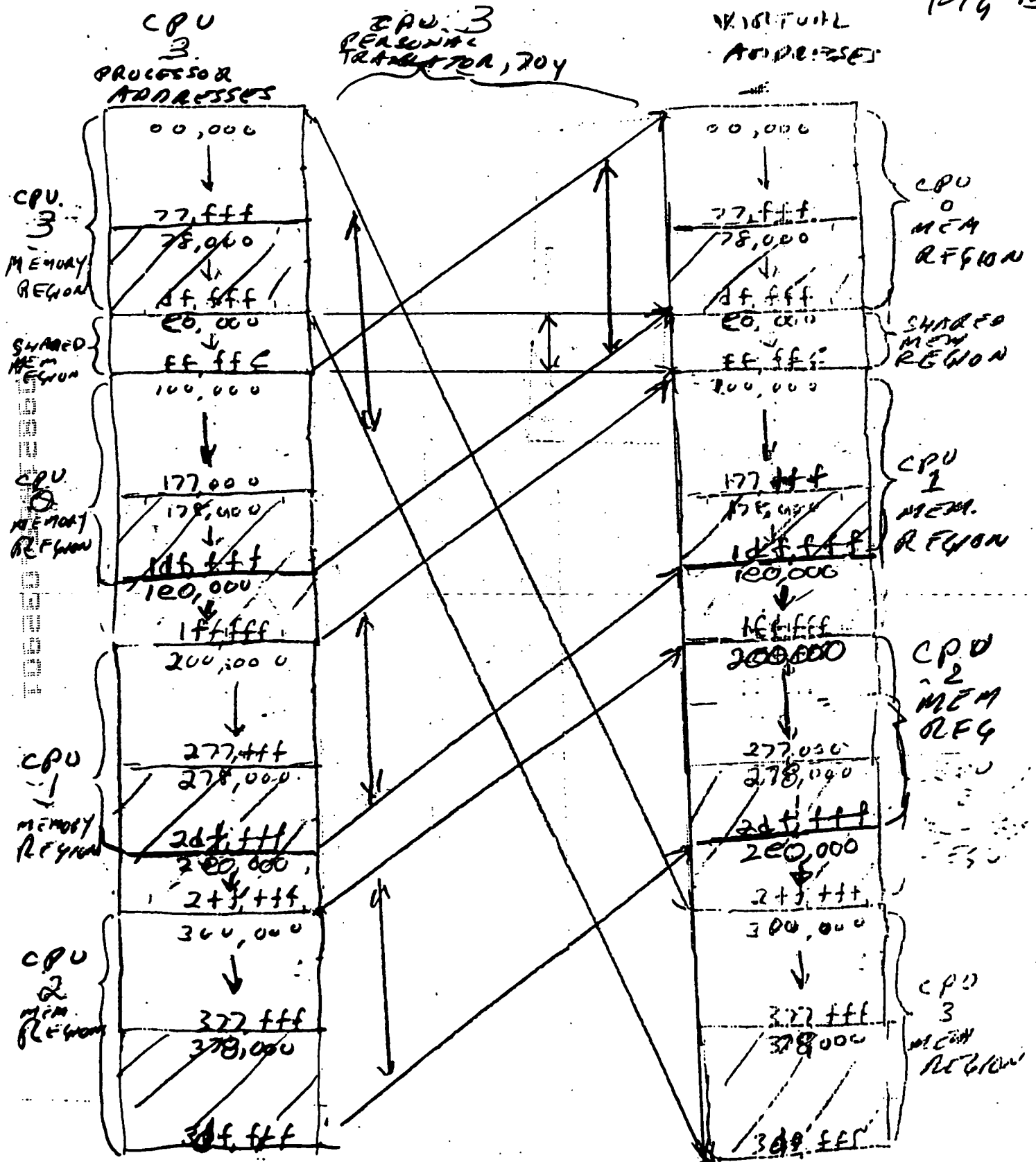






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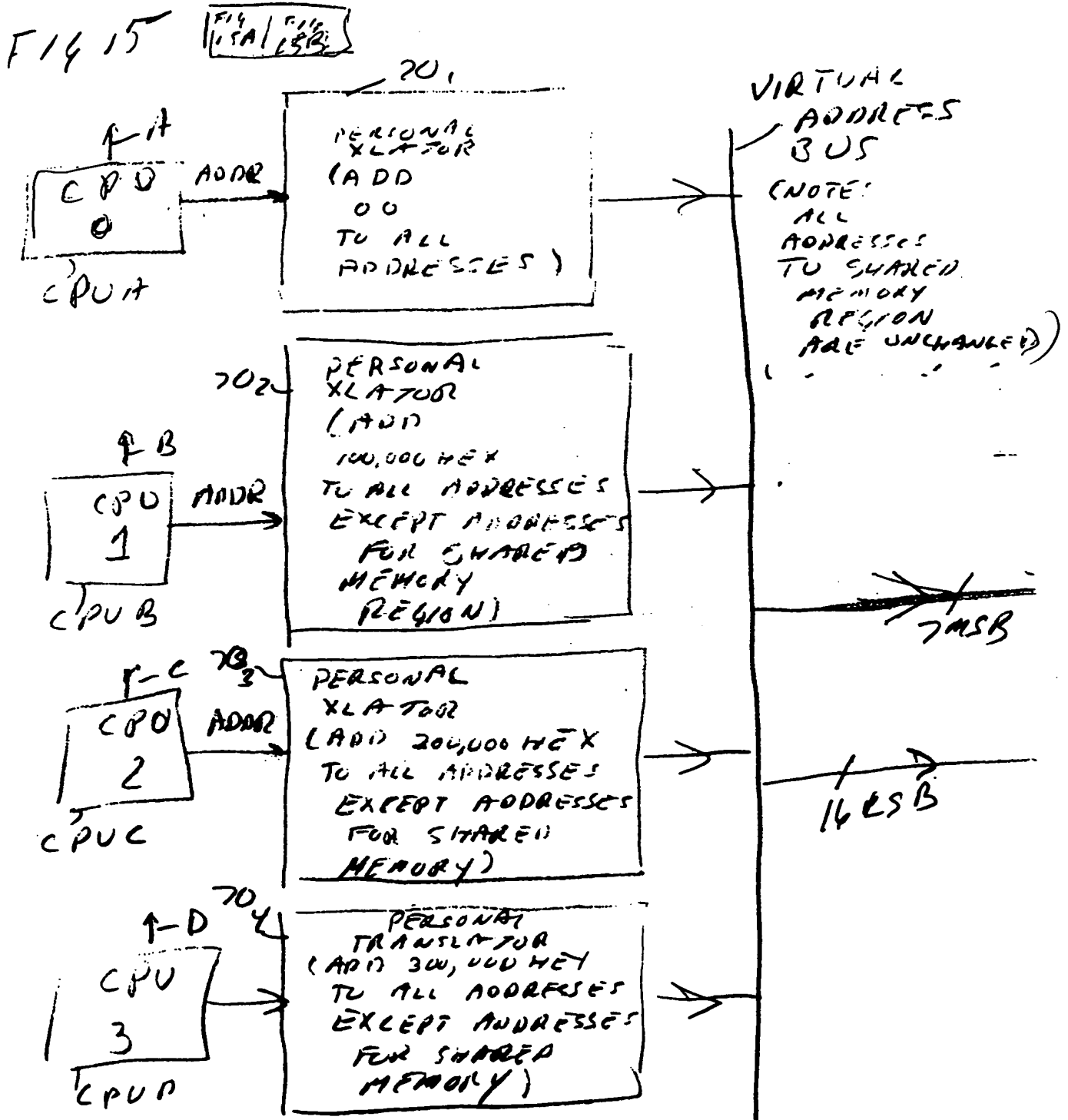


FIG 15A

15B

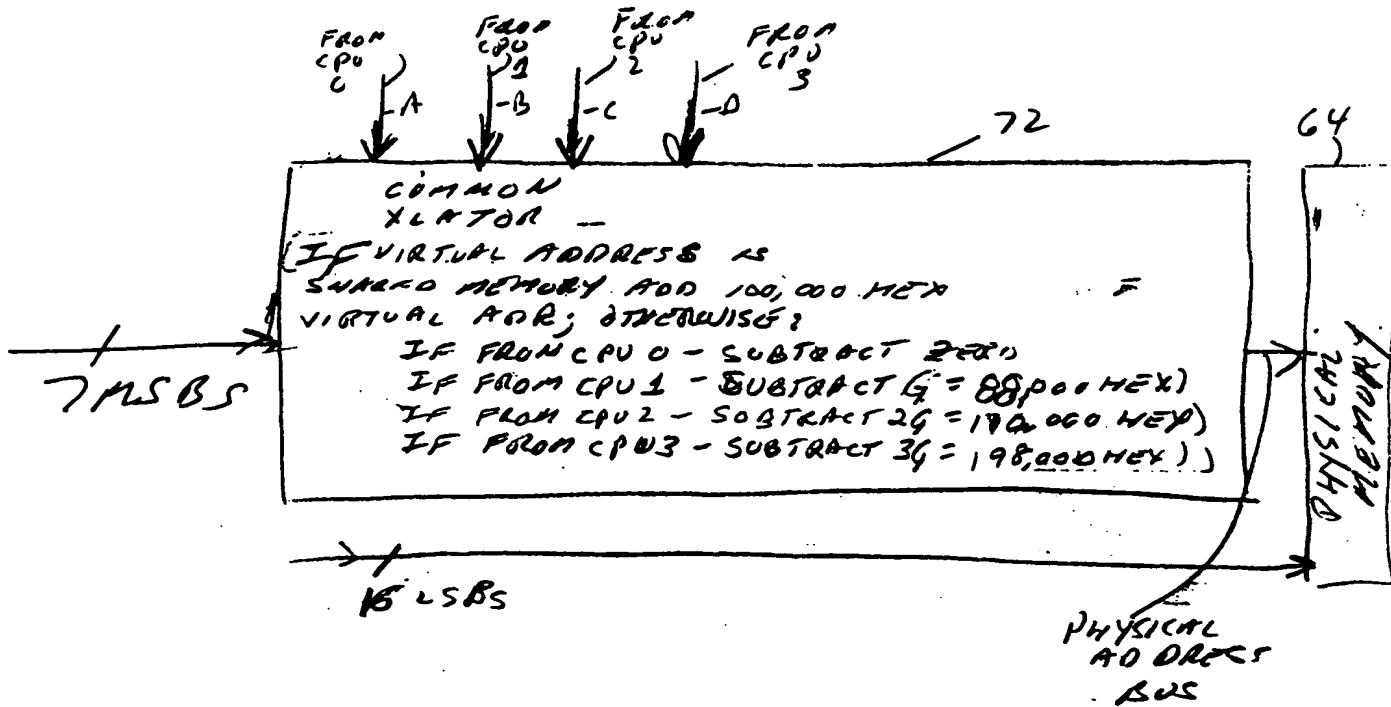


FIG 15B

FIG 16

VIRTUAL
ADDRESSES

MEMORY
(PHYSICAL)
ADDRESSES

COMMON TRANSLATOR

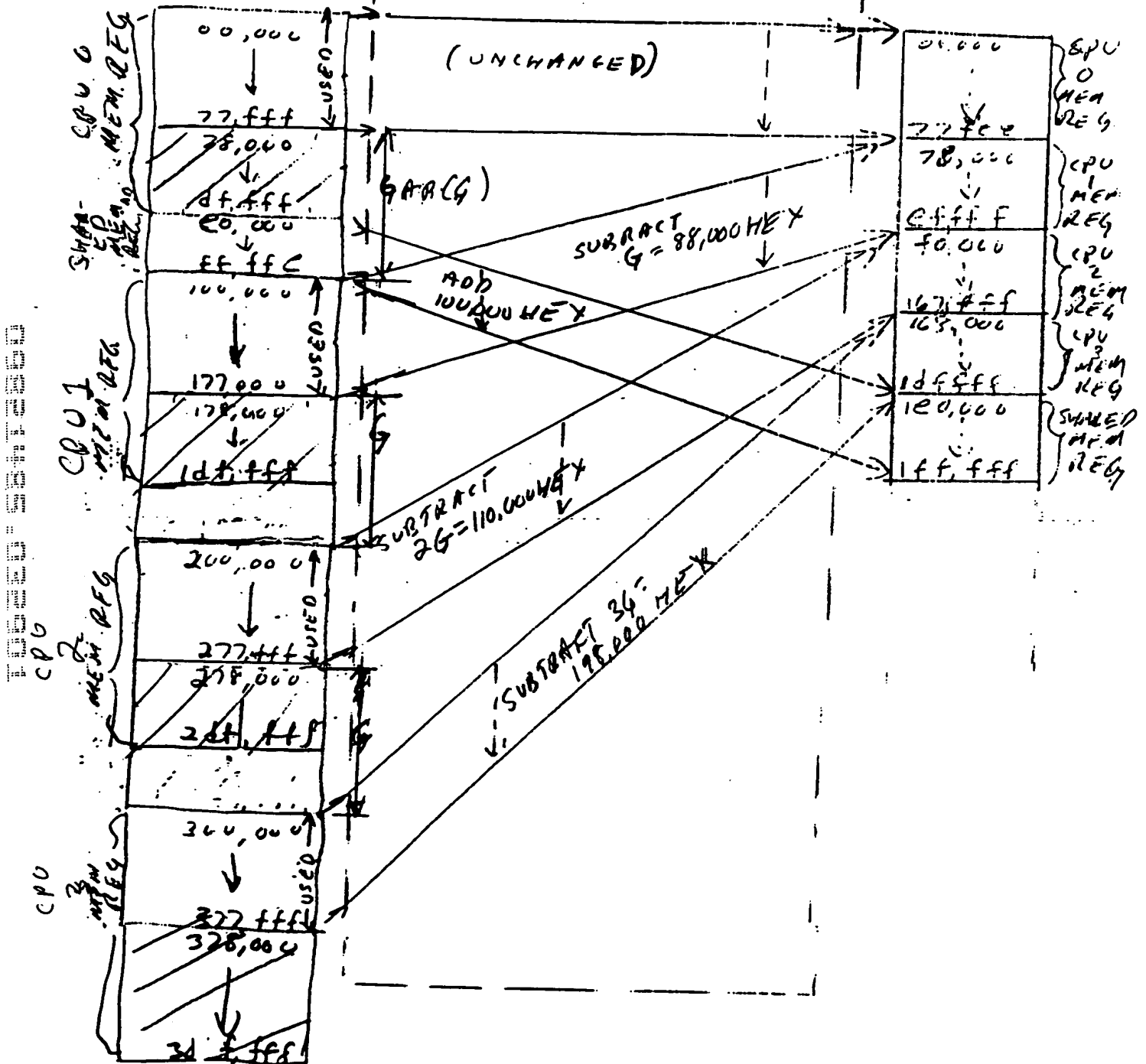


FIG. 17

PROGRAM STORED IN EACH
PERSONAL TRANSLATOR

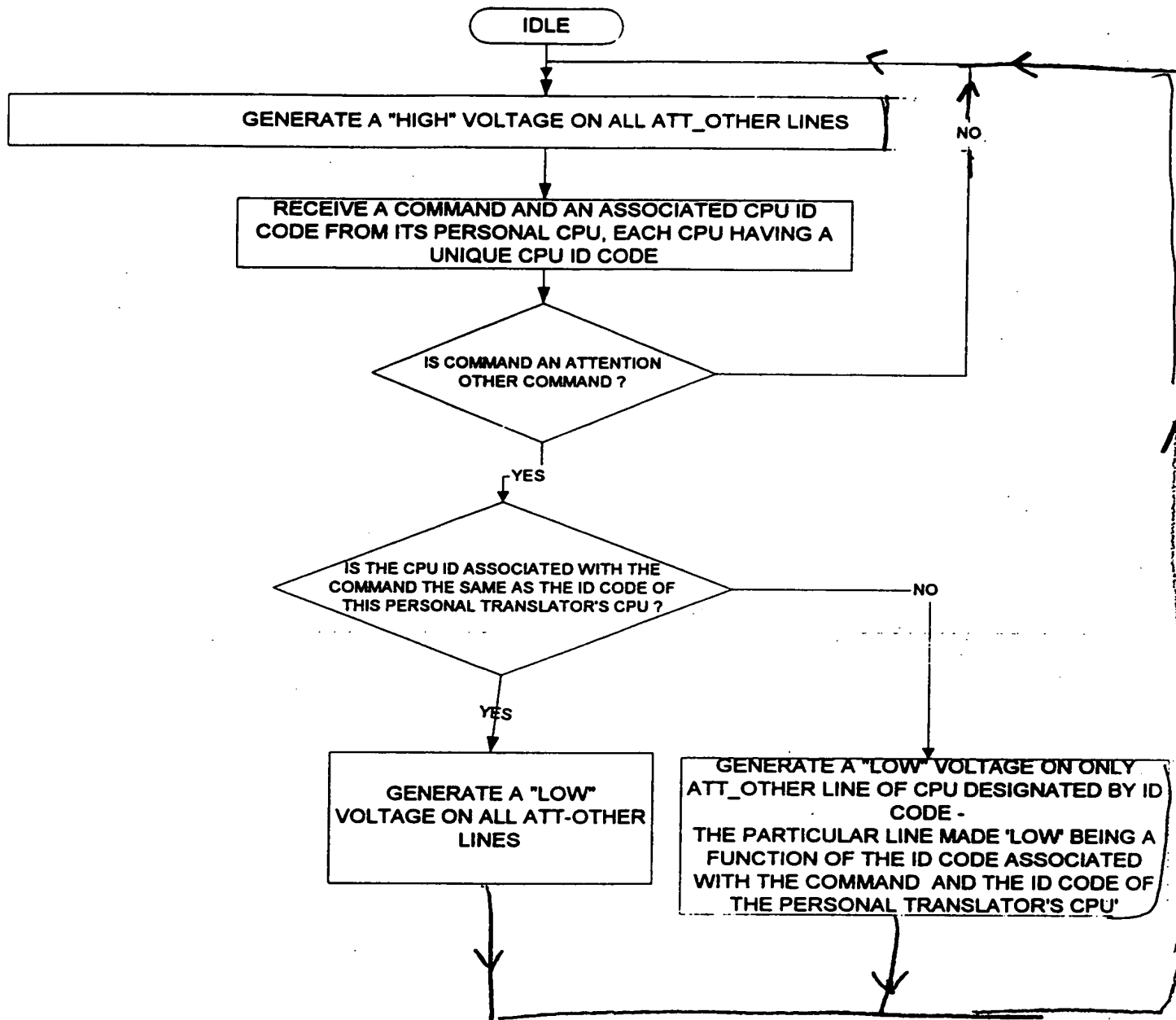


FIG. 17

FIG. 18

*PROGRAM IN COMMON
TRANSLATOR*

